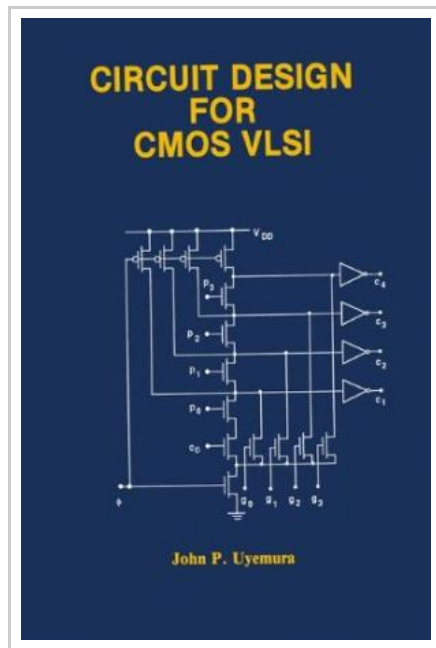




Circuit Design for CMOS VLSI

By Uyemura, John P.

Book Condition: New. Publisher/Verlag: Springer, Berlin | During the last decade, CMOS has become increasingly attractive as a basic integrated circuit technology due to its low power (at moderate frequencies), good scalability, and rail-to-rail operation. There are now a variety of CMOS circuit styles, some based on static complementary con ductance properties, but others borrowing from earlier NMOS techniques and the advantages of using clocking disciplines for precharge-evaluate se quencing. In this comprehensive book, the reader is led systematically through the entire range of CMOS circuit design. Starting with the in dividual MOSFET, basic circuit building blocks are described, leading to a broad view of both combinatorial and sequential circuits. Once these circuits are considered in the light of CMOS process technologies, impor tant topics in circuit performance are considered, including characteristics of interconnect, gate delay, device sizing, and I/O buffering. Basic circuits are then composed to form macro elements such as multipliers, where the reader acquires a unified view of architectural performance through par allelism, and circuit performance through careful attention to circuit-level and layout design optimization. Topics in analog circuit design reflect the growing tendency for both analog and digital circuit forms to be combined on the same...



READ ONLINE
[1.73 MB]

Reviews

This pdf is indeed gripping and exciting. It is writter in easy words and phrases and not confusing. Once you begin to read the book, it is extremely difficult to leave it before concluding.

-- **Alayna Kuphal**

This composed pdf is excellent. It really is basic but excitement in the 50 % in the book. Your lifestyle span will likely be change when you comprehensive looking at this book.

-- **Tom Fisher**